



AXIOMISE FORMAL VERIFICATION TRAINING PROGRAMES AT A GLANCE

elearn.axiomise.com

Email us at: training@axiomise.com for pricing information

All courses can be taken independent of each other. Only pre-requisite is familiarity with digital design and knowledge of VHDL/Verilog

	ON-DEMAND, SELF-PACED	INSTRUCTOR-LED					
	FORMAL VERIFICATION 101	BRONZE	BRONZE+	SILVER	GOLD	GOLD+	PLATINUM
Description	INTRODUCTORY	BEGINNER	INTERMEDIATE	ADVANCED	COMPREHENSIVE	COMPREHENSIVE	ULTIMATE
Pre-requisite	VERILOG/VHDL	VERILOG/VHDL	VERILOG/VHDL	VERILOG/VHDL	VERILOG/VHDL	VERILOG/VHDL	VERILOG/VHDL
Days	FLEXIBLE, SELF-PACED	1	2	3	4	4	5
Graded Exam	✓	✗	✓	✓	✓	✓	✓
Certificate	✓	✓	✓	✓	✓	✓	✓
Theory	✓	✓	✓	✓	✓	✓	✓
Labs	✗	✗	✓	✓	✓	✓	✓
No. of Labs	0	0	1	4	6	5	7
Demo	✓	✓	✓	✓	✓	✓	✓
RISC-V Case Study	✓	✗	✓	✓	✓	✓	✓
Coverage & sign-off	✓	✓	✓	✓	✓	✓	✓
Customer specific designs	✗	✗	✗	✗	✗	✓	✓
THEOREM PROVING	THEORY & DEMOS	THEORY	THEORY	THEORY	THEORY	THEORY	THEORY
PROPERTY CHECKING	THEORY & DEMOS	THEORY	THEORY+LABS	THEORY+LABS	THEORY+LABS	THEORY+LABS	THEORY+LABS
EQUIVALENCE CHECKING	THEORY & DEMOS	THEORY	THEORY	THEORY	THEORY+LABS	THEORY	THEORY+LABS